

Microprocessors

Review: Components of a Microprocessor

What are they?

Components of a Microprocessor

- Memory:
 - Storage of data
 - Storage of a program
 - Either can be temporary or “permanent” storage
- Registers: small, fast memories
 - General purpose: temporarily store arbitrary data
 - Special purpose: used to control the processor

Components of a Microprocessor

- Instruction decoder:
 - Translates current program instruction into a set of control signals
- Arithmetic logical unit:
 - Performs both arithmetic and logical operations on data: add, subtract, multiply, AND, OR ...
- Floating point unit
- Input/output control modules

Components of a Microprocessor

- Many of these components must exchange data with one-another
- It is common to use a 'bus' for this exchange

Buses

- In the simplest form, a bus is a single wire
- Many different components can be attached to the bus
- Any component can take input from the bus or place information on the bus

Buses

- At most one component may write to the bus at any one time
- In a microprocessor, which component is allowed to write is usually determined by the code that is currently executing

Machine-Level Programs

Machine-level programs are stored as sequences of *atomic* machine instructions

- Stored in program memory
- Execution is generally sequential (instructions are executed in order)
- But – with occasional “jumps” to other locations in memory

Types of Instructions

- Memory operations: transfer data values between memory and the internal registers
- Mathematical operations: ADD, SUBTRACT, MULT, AND, etc.
- Tests: $\text{value} == 0$, $\text{value} > 0$, etc.
- Program flow: jump to a new location, jump conditionally (e.g., if the last test was true)

Atmel Instructions

Some Mega2560 Memory Operations

LDS Rd, k

We refer to this as
“Assembly Language”

- Load SRAM memory location k into register Rd
- $Rd \leftarrow (k)$

STS Rd, k

- Store value of Rd into SRAM location k
- $(k) \leftarrow Rd$

Some Mega2560 Arithmetic and Logical Instructions

ADD Rd, Rr

- Add Rd and Rr (these are registers)
- Operation: $Rd \leftarrow Rd + Rr$

ADC Rd, Rr

- Add with carry
- $Rd \leftarrow Rd + Rr + C$

Some Mega2560 Arithmetic and Logical Instructions

NEG Rd: take the two's complement of Rd

AND Rd, Rr: bit-wise AND with a register

ANDI Rd, K: bit-wise AND with a constant

EOR Rd, Rr: bit-wise XOR

INC Rd: increment Rd

MUL Rd, Rr: multiply Rd and Rr (unsigned)

MULS Rd, Rr: multiply (signed)

Connecting Assembly Language to C

- Our C compiler is responsible for translating our code into Assembly Language
- Today, we rarely program in Assembly Language
 - Embedded systems are a common exception
 - Also: it is useful in some cases to view the assembly code generated by the compiler

An Example

A C code snippet:

```
if(B < A) {  
    D += A;  
}
```

An Example

A C code snippet:

```
if(B < A) {  
    D += A;  
}
```

The Assembly :

```
LDS R1 (A)  
LDS R2 (B)  
CP R2, R1  
BRGE 3  
LDS R3 (D)  
ADD R3, R1  
STS (D), R3
```

.....

An Example

A C code snippet:

```
if(B < A) {  
    D += A;  
}
```

Load the contents of memory
location A into register 1

The Assembly :

LDS R1 (A) ← PC

LDS R2 (B)

CP R2, R1

BRGE 3

LDS R3 (D)

ADD R3, R1

STS (D), R3

.....

An Example

A C code snippet:

```
if(B < A) {  
    D += A;  
}
```

Load the contents of memory
location B into register 2

The Assembly :

LDS R1 (A)

LDS R2 (B) ← PC

CP R2, R1

BRGE 3

LDS R3 (D)

ADD R3, R1

STS (D), R3

.....

An Example

A C code snippet:

```
if(B < A) {  
    D += A;  
}
```

Compare the contents of register
2 with those of register 1

This results in a change to the
status register

The Assembly :

LDS R1 (A)

LDS R2 (B)

CP R2, R1 ← **PC**

BRGE 3

LDS R3 (D)

ADD R3, R1

STS (D), R3

.....

An Example

A C code snippet:

```
if(B < A) {  
    D += A;  
}
```

Branch If Greater Than or Equal To:
jump ahead 3 instructions if true

The Assembly :

LDS R1 (A)

LDS R2 (B)

CP R2, R1

BRGE 3

LDS R3 (D)

ADD R3, R1

STS (D), R3

.....

← PC

An Example

A C code snippet:

```
if(B < A) {  
    D += A;  
}
```

Branch if greater than or equal to
will jump ahead 3 instructions if
true

The Assembly :

LDS R1 (A)

LDS R2 (B)

CP R2, R1

BRGE 3

LDS R3 (D)

ADD R3, R1

STS (D), R3

.....

if true

PC

An Example

The Assembly :

A C code snippet:

```
if(B < A) {  
    D += A;  
}
```

if not true



LDS R1 (A)

LDS R2 (B)

CP R2, R1

BRGE 3

LDS R3 (D)

← PC

ADD R3, R1

STS (D), R3

.....

Not true: execute the next instruction

An Example

A C code snippet:

```
if(B < A) {  
    D += A;  
}
```

Load the contents of memory
location D into register 3

The Assembly :

LDS R1 (A)

LDS R2 (B)

CP R2, R1

BRGE 3

LDS R3 (D) ← PC

ADD R3, R1

STS (D), R3

.....

An Example

A C code snippet:

```
if(B < A) {  
    D += A;  
}
```

Add the values in
registers 1 and 3 and
store the result in
register 3

The Assembly :

LDS R1 (A)

LDS R2 (B)

CP R2, R1

BRGE 3

LDS R3 (D)

 ADD R3, R1  **PC**

STS (D), R3

.....

An Example

A C code snippet:

```
if(B < A) {  
    D += A;  
}
```

Store the value in register
3 back to memory
location D

The Assembly :

LDS R1 (A)

LDS R2 (B)

CP R2, R1

BRGE 3

LDS R3 (D)

ADD R3, R1

STS (D), R3 ← PC

.....

Take-Aways

Instructions are the “atomic” actions that are taken by the processor

- Many different component work together to execute a single instruction
- One line of C code typically translates into a sequence of several instructions
- In the Teensy, most instructions are executed in a single clock cycle

The high-level view is important here: you won't be compiling programs on exams

An Example

```
#include "oulib.h"

volatile uint8_t a = 10;

int main (void)
{
    a = a+5;

    while(1) {
        delay_ms(++a);
    };
}
```

Volatile Keyword

```
uint8_t a = 10;
```

- If variable 'a' is accessed multiple times in a row, then it stays in a general purpose register during all of the accesses

```
volatile uint8_t a = 10;
```

- Every time variable 'a' is accessed, it is fetched from memory

0000013c <main>:

```
volatile uint8_t a = 10;
```

```
int main (void)
```

```
{
```

```
    a = a+5;
```

```
13c:    80 91 00 02    lds     r24, 0x0200
140:    8b 5f          subi    r24, 0xFB          ; 251
142:    80 93 00 02    sts     0x0200, r24
```

```
    while(1) {
```

```
        delay_ms(++a);
```

```
146:    80 91 00 02    lds     r24, 0x0200
14a:    8f 5f          subi    r24, 0xFF          ; 255
14c:    80 93 00 02    sts     0x0200, r24
150:    80 91 00 02    lds     r24, 0x0200
154:    90 e0          ldi     r25, 0x00          ; 0
156:    0e 94 ae 00    call    0x15c             ; 0x15c <delay_ms>
15a:    f5 cf          rjmp    .-22             ; 0x146 <main+0xa>
```

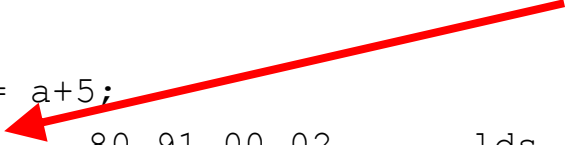
Compiled Result

```
0000013c <main>:
```

```
volatile uint8_t a = 10;
```

```
int main (void)
{
```

```
    a = a+5;
```



13c:	80 91 00 02	lds	r24, 0x0200	
140:	8b 5f	subi	r24, 0xFB	; 251
142:	80 93 00 02	sts	0x0200, r24	

```
    while(1) {
```

```
        delay_ms(++a);
```

146:	80 91 00 02	lds	r24, 0x0200	
14a:	8f 5f	subi	r24, 0xFF	; 255
14c:	80 93 00 02	sts	0x0200, r24	
150:	80 91 00 02	lds	r24, 0x0200	
154:	90 e0	ldi	r25, 0x00	; 0
156:	0e 94 ae 00	call	0x15c	; 0x15c <delay_ms>
15a:	f5 cf	rjmp	.-22	; 0x146 <main+0xa>

Compiled Result

Location in program memory

Compiled Result

```
0000013c <main>:
```

```
volatile uint8_t a = 10;
```

```
int main (void)
```

```
{
```

```
    a = a+5;
```

```
13c:    80 91 00 02
```

```
lds    r24, 0x0200
```

```
140:    8b 5f
```

```
subi    r24, 0xFB        ; 251
```

```
142:    80 93 00 02
```

```
sts    0x0200, r24
```

**Load memory
location 0x200 to r24**



```
    while(1) {
```

```
        delay_ms(++a);
```

```
146:    80 91 00 02
```

```
lds    r24, 0x0200
```

```
14a:    8f 5f
```

```
subi    r24, 0xFF        ; 255
```

```
14c:    80 93 00 02
```

```
sts    0x0200, r24
```

```
150:    80 91 00 02
```

```
lds    r24, 0x0200
```

```
154:    90 e0
```

```
ldi    r25, 0x00        ; 0
```

```
156:    0e 94 ae 00
```

```
call    0x15c            ; 0x15c <delay_ms>
```

```
15a:    f5 cf
```

```
rjmp    .-22            ; 0x146 <main+0xa>
```

Compiled Result

```
0000013c <main>:
```

```
volatile uint8_t a = 10;
```

```
int main (void)
```

```
{
```

```
    a = a+5;
```

```
13c:    80 91 00 02
```

```
lds     r24, 0x0200
```

```
140:    8b 5f
```

```
subi    r24, 0xFB ; 251
```

```
142:    80 93 00 02
```

```
sts     0x0200, r24
```

Add 5 to r24



```
    while(1) {
```

```
        delay_ms(++a);
```

```
146:    80 91 00 02
```

```
lds     r24, 0x0200
```

```
14a:    8f 5f
```

```
subi    r24, 0xFF ; 255
```

```
14c:    80 93 00 02
```

```
sts     0x0200, r24
```

```
150:    80 91 00 02
```

```
lds     r24, 0x0200
```

```
154:    90 e0
```

```
ldi     r25, 0x00 ; 0
```

```
156:    0e 94 ae 00
```

```
call    0x15c ; 0x15c <delay_ms>
```

```
15a:    f5 cf
```

```
rjmp    .-22 ; 0x146 <main+0xa>
```

Compiled Result

```
0000013c <main>:
```

```
volatile uint8_t a = 10;
```

```
int main (void)
```

```
{
```

```
    a = a+5;
```

```
13c:    80 91 00 02
```

```
lds     r24, 0x0200
```

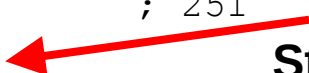
```
140:    8b 5f
```

```
subi    r24, 0xFB          ; 251
```

```
142:    80 93 00 02
```

```
sts     0x0200, r24
```

**Store r24 to memory
location 0x200**



```
    while(1) {
```

```
        delay_ms(++a);
```

```
146:    80 91 00 02
```

```
lds     r24, 0x0200
```

```
14a:    8f 5f
```

```
subi    r24, 0xFF          ; 255
```

```
14c:    80 93 00 02
```

```
sts     0x0200, r24
```

```
150:    80 91 00 02
```

```
lds     r24, 0x0200
```

```
154:    90 e0
```

```
ldi     r25, 0x00          ; 0
```

```
156:    0e 94 ae 00
```

```
call    0x15c              ; 0x15c <delay_ms>
```

```
15a:    f5 cf
```

```
rjmp    .-22              ; 0x146 <main+0xa>
```

Compiled Result

```
0000013c <main>:
```

```
volatile uint8_t a = 10;
```

```
int main (void)
```

```
{
```

```
    a = a+5;
```

```
13c:    80 91 00 02    lds     r24, 0x0200
140:    8b 5f          subi    r24, 0xFB          ; 251
142:    80 93 00 02    sts     0x0200, r24
```

```
    while(1) {
```

```
        delay_ms(++a);
```

```
146:    80 91 00 02    lds     r24, 0x0200
14a:    8f 5f          subi    r24, 0xFF          ; 255
14c:    80 93 00 02    sts     0x0200, r24
150:    80 91 00 02    lds     r24, 0x0200
154:    90 e0          ldi     r25, 0x00          ; 0
156:    0e 94 ae 00    call    0x15c              ; 0x15c <delay_ms>
15a:    f5 cf          rjmp    .-22              ; 0x146 <main+0xa>
```

**Load memory
location 0x200 to r24**



Compiled Result

```
0000013c <main>:
```

```
volatile uint8_t a = 10;
```

```
int main (void)
```

```
{
```

```
    a = a+5;
```

```
13c:    80 91 00 02    lds     r24, 0x0200
140:    8b 5f          subi    r24, 0xFB          ; 251
142:    80 93 00 02    sts     0x0200, r24
```

```
    while(1) {
```

```
        delay_ms(++a);
```

```
146:    80 91 00 02    lds     r24, 0x0200
14a:    8f 5f          subi    r24, 0xFF          ; 255
14c:    80 93 00 02    sts     0x0200, r24
150:    80 91 00 02    lds     r24, 0x0200
154:    90 e0          ldi     r25, 0x00          ; 0
156:    0e 94 ae 00    call    0x15c             ; 0x15c <delay_ms>
15a:    f5 cf          rjmp    .-22             ; 0x146 <main+0xa>
```

Add 1 to r24



Compiled Result

```
0000013c <main>:
```

```
volatile uint8_t a = 10;
```

```
int main (void)
```

```
{
```

```
    a = a+5;
```

```
13c:    80 91 00 02    lds     r24, 0x0200
140:    8b 5f          subi    r24, 0xFB          ; 251
142:    80 93 00 02    sts     0x0200, r24
```

```
    while(1) {
```

```
        delay_ms(++a);
```

```
146:    80 91 00 02    lds     r24, 0x0200
14a:    8f 5f          subi    r24, 0xFF          ; 255
14c:    80 93 00 02    sts     0x0200, r24
150:    80 91 00 02    lds     r24, 0x0200
154:    90 e0          ldi     r25, 0x00          ; 0
156:    0e 94 ae 00    call    0x15c              ; 0x15c <delay_ms>
15a:    f5 cf          rjmp    .-22              ; 0x146 <main+0xa>
```

 **Store r24 to memory
location 0x200**

Compiled Result

```
0000013c <main>:
```

```
volatile uint8_t a = 10;
```

```
int main (void)
```

```
{
```

```
    a = a+5;
```

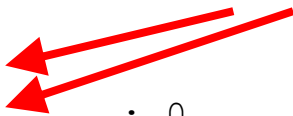
```
13c:    80 91 00 02    lds     r24, 0x0200
140:    8b 5f          subi    r24, 0xFB          ; 251
142:    80 93 00 02    sts     0x0200, r24
```

```
    while(1) {
```

```
        delay_ms(++a);
```

```
146:    80 91 00 02    lds     r24, 0x0200
14a:    8f 5f          subi    r24, 0xFF          ; 255
14c:    80 93 00 02    sts     0x0200, r24
150:    80 91 00 02    lds     r24, 0x0200
154:    90 e0          ldi     r25, 0x00          ; 0
156:    0e 94 ae 00    call    0x15c             ; 0x15c <delay_ms>
15a:    f5 cf          rjmp    .-22             ; 0x146 <main+0xa>
```

**Load memory
location 0x200 to
r25, r24**



Compiled Result

```
0000013c <main>:
```

```
volatile uint8_t a = 10;
```

```
int main (void)
```

```
{
```

```
    a = a+5;
```

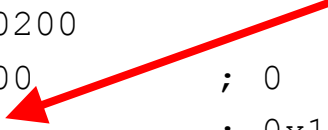
```
13c:    80 91 00 02    lds     r24, 0x0200
140:    8b 5f          subi    r24, 0xFB          ; 251
142:    80 93 00 02    sts     0x0200, r24
```

```
    while(1) {
```

```
        delay_ms(++a);
```

```
146:    80 91 00 02    lds     r24, 0x0200
14a:    8f 5f          subi    r24, 0xFF          ; 255
14c:    80 93 00 02    sts     0x0200, r24
150:    80 91 00 02    lds     r24, 0x0200
154:    90 e0          ldi     r25, 0x00          ; 0
156:    0e 94 ae 00    call    0x15c              ; 0x15c <delay_ms>
15a:    f5 cf          rjmp    .-22              ; 0x146 <main+0xa>
```

Call delay_ms()



Compiled Result

```
0000013c <main>:
```

```
volatile uint8_t a = 10;
```

```
int main (void)
```

```
{
```

```
    a = a+5;
```

```
13c:    80 91 00 02    lds     r24, 0x0200
140:    8b 5f          subi    r24, 0xFB          ; 251
142:    80 93 00 02    sts     0x0200, r24
```

```
    while(1) {
```

```
        delay_ms(++a);
```

```
146:    80 91 00 02    lds     r24, 0x0200
14a:    8f 5f          subi    r24, 0xFF          ; 255
14c:    80 93 00 02    sts     0x0200, r24
150:    80 91 00 02    lds     r24, 0x0200
154:    90 e0          ldi     r25, 0x00          ; 0
156:    0e 94 ae 00    call    0x15c              ; 0x15c <delay_ms>
15a:    f5 cf          rjmp    .-22              ; 0x146 <main+0xa>
```



**Go back to top of
while() loop**

Example II

```
#include "oulib.h"

volatile uint16_t a = 10;

int main (void)
{
    a = a+5;

    while(1) {
        delay_ms(++a);
    };
}
```

Example II

```
#include "oulib.h"
```

```
volatile uint16_t a = 10;
```

```
int main (void)
```

```
{
```

```
    a = a+5;
```

```
    while(1) {
```

```
        delay_ms(++a);
```

```
    };
```

```
}
```

**Size of
integer has
changed!**

**We need
two bytes**

```
0000013c <main>:
```

```
volatile uint16_t a = 10;
```

```
int main (void)
```

```
{
```

```
    a = a+5;
```

```
13c:    80 91 00 02    lds    r24, 0x0200
140:    90 91 01 02    lds    r25, 0x0201
144:    05 96         adiw    r24, 0x05        ; 5
146:    90 93 01 02    sts    0x0201, r25
14a:    80 93 00 02    sts    0x0200, r24
```

```
    while(1) {
```

```
        delay_ms(++a);
```

```
14e:    80 91 00 02    lds    r24, 0x0200
152:    90 91 01 02    lds    r25, 0x0201
156:    01 96         adiw    r24, 0x01        ; 1
158:    90 93 01 02    sts    0x0201, r25
15c:    80 93 00 02    sts    0x0200, r24
160:    80 91 00 02    lds    r24, 0x0200
164:    90 91 01 02    lds    r25, 0x0201
168:    0e 94 b7 00    call   0x16e        ; 0x16e <delay_ms>
16c:    f0 cf         rjmp    .-32        ; 0x14e <main+0x12>
```

Compiled Result

```

0000013c <main>:
volatile uint16_t a = 10;
int main (void)
{

```

Compiled Result

Load memory locations
0x201, 0x200
to r25, r24

```

13c:      80 91 00 02      lds      r24, 0x0200
140:      90 91 01 02      lds      r25, 0x0201
144:      05 96           adiw      r24, 0x05          ; 5
146:      90 93 01 02      sts      0x0201, r25
14a:      80 93 00 02      sts      0x0200, r24

while(1) {
    delay_ms(++a);
14e:      80 91 00 02      lds      r24, 0x0200
152:      90 91 01 02      lds      r25, 0x0201
156:      01 96           adiw      r24, 0x01          ; 1
158:      90 93 01 02      sts      0x0201, r25
15c:      80 93 00 02      sts      0x0200, r24
160:      80 91 00 02      lds      r24, 0x0200
164:      90 91 01 02      lds      r25, 0x0201
168:      0e 94 b7 00      call     0x16e          ; 0x16e <delay_ms>
16c:      f0 cf           rjmp     .-32          ; 0x14e <main+0x12>

```

```

0000013c <main>:
volatile uint16_t a = 10;
int main (void)
{

```

Compiled Result

a = a+5;				Add 5 to r25, r24
13c:	80 91 00 02	lds	r24, 0x0200	
140:	90 91 01 02	lds	r25, 0x0201	
144:	05 96	adiw	r24, 0x05	; 5
146:	90 93 01 02	sts	0x0201, r25	
14a:	80 93 00 02	sts	0x0200, r24	
while(1) {				
delay_ms(++a);				
14e:	80 91 00 02	lds	r24, 0x0200	
152:	90 91 01 02	lds	r25, 0x0201	
156:	01 96	adiw	r24, 0x01	; 1
158:	90 93 01 02	sts	0x0201, r25	
15c:	80 93 00 02	sts	0x0200, r24	
160:	80 91 00 02	lds	r24, 0x0200	
164:	90 91 01 02	lds	r25, 0x0201	
168:	0e 94 b7 00	call	0x16e	; 0x16e <delay_ms>
16c:	f0 cf	rjmp	.-32	; 0x14e <main+0x12>

```

0000013c <main>:
volatile uint16_t a = 10;
int main (void)
{

```

Compiled Result

```

    a = a+5;

```

```

13c:      80 91 00 02      lds      r24, 0x0200
140:      90 91 01 02      lds      r25, 0x0201
144:      05 96           adiw      r24, 0x05           ; 5
146:      90 93 01 02      sts      0x0201, r25
14a:      80 93 00 02      sts      0x0200, r24

```

**Store r25, r24 to
memory locations
0x201, 0x200**



```

    while(1) {

```

```

        delay_ms(++a);

```

```

14e:      80 91 00 02      lds      r24, 0x0200
152:      90 91 01 02      lds      r25, 0x0201
156:      01 96           adiw      r24, 0x01           ; 1
158:      90 93 01 02      sts      0x0201, r25
15c:      80 93 00 02      sts      0x0200, r24
160:      80 91 00 02      lds      r24, 0x0200
164:      90 91 01 02      lds      r25, 0x0201
168:      0e 94 b7 00      call     0x16e           ; 0x16e <delay_ms>
16c:      f0 cf           rjmp     .-32           ; 0x14e <main+0x12>

```

```

0000013c <main>:
volatile uint16_t a = 10;
int main (void)
{

```

Compiled Result

```

    a = a+5;

```

```

13c:      80 91 00 02      lds      r24, 0x0200
140:      90 91 01 02      lds      r25, 0x0201
144:      05 96           adiw      r24, 0x05           ; 5
146:      90 93 01 02      sts      0x0201, r25
14a:      80 93 00 02      sts      0x0200, r24

```

**Store r25, r24 to
memory locations
0x201, 0x200**



```

    while(1) {

```

```

        delay_ms(++a);

```

```

14e:      80 91 00 02      lds      r24, 0x0200
152:      90 91 01 02      lds      r25, 0x0201
156:      01 96           adiw      r24, 0x01           ; 1
158:      90 93 01 02      sts      0x0201, r25
15c:      80 93 00 02      sts      0x0200, r24
160:      80 91 00 02      lds      r24, 0x0200
164:      90 91 01 02      lds      r25, 0x0201
168:      0e 94 b7 00      call     0x16e           ; 0x16e <delay_ms>
16c:      f0 cf           rjmp     .-32           ; 0x14e <main+0x12>

```

**We have doubled
the number of
memory
operations!**

Take-Home Message I

We want to carefully choose our data types

- Smaller variables are handled more efficiently
- But: we need to make sure that the results of the math that we do with these variables fits in the size that we have chosen
 - Intermediate values must fit, too!

Take-Home Message II

- A line of C code usually translates into a sequence of atomic instructions
- Most instructions are executed in one cycle of the system clock
- For a given instruction, many different components work together to make that instruction happen
 - Program counter, general and special purpose registers, memory, ALU, FPU

Take-Home Message III

- You should know what these different components are and what they do at an abstract level
- You don't need to know the details of the assembly language or how these details relate to specific lines of C code